IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended): A liquid crystal display, comprising:

a liquid crystal polarity inversion driver determining whether a polarity of a liquid crystal is inverted and inverting the polarity of the liquid crystal in accordance with the determined result;

a first data polarity inversion driver determining whether a first data transition [[is]] has occurred in a first set ~~data~~ of ~~input~~ data, and inverting the polarity of the first set of data in accordance with the determined result; and

a second data polarity inversion driver determining whether a second data transition [[is]] has occurred in a second set ~~data~~ of ~~the input~~ data and inverting the polarity of the second set of data in accordance with the determined result,

wherein the first set of data is odd-numbered bits in ~~the~~ an input data and the second set of data is even-numbered bits in the input data.

2. (Currently Amended): The liquid crystal display according to claim 1, wherein the first data polarity inversion driver includes: [[,]]

a first data transition part determining whether the first data transition [[is]] has occurred in the first set of data and outputting a first signal;

a first data polarity inversion signal summer counting the number of the first signal that a data polarity [[is]] has changed according to the first data transition and determining whether an

output level is high or low; and

a first data polarity inversion signal output part receiving the first signal and the determined output level from the first data transition part and the first data polarity inversion signal summer and outputting an inverting signal for inverting output data.

3. (Currently Amended): The liquid crystal display according to claim 1, wherein the second data polarity inversion driver includes: [[,]]

a second data transition part determining whether the second data transition [[is]] has occurred in the second set of data and outputting a second signal;

a second data polarity inversion signal summer counting the number of the second signal that a data polarity [[is]] has changed according to the second data transition and determining whether an output level is high or low; and

a second data polarity inversion signal output part receiving the second signal and the determined output level from the second data transition part and the second data polarity inversion signal summer and outputting an inverting signal for inverting output data.

4. (Currently Amended): The liquid crystal display according to claim 2, wherein the first data transition part includes first and second flip-flops and an exclusive logical sum gate comparing current data with previous data to determine whether the first data transition [[is]] has occurred in accordance with the compared result.

5. (Currently Amended): The liquid crystal display according to claim 3, wherein the second data transition part includes first and second flip-flops and an exclusive logical sum gate comparing current data with previous data to determine whether the second data transition [[is]] has occurred in accordance with the compared result.

6. (Currently Amended): The liquid crystal display according to claim 2, wherein the first data polarity inversion signal summer includes: [[,]]

an adder adding the number of data with a data transition from the first data transition part; and

a majority detector determining whether the added number of the data is higher than a first reference value.

7. (Currently Amended): The liquid crystal display according to claim 3, wherein the second data polarity inversion signal summer includes: [[,]]

an adder adding the number of data with a data transition from the second data transition part; and

a majority detector determining whether the added number of the data is higher than a second reference value.

8. (Currently Amended): The liquid crystal display according to claim 2, wherein the first data polarity inversion signal output part includes[[,]] a multiplexor receiving a first polarity inversion signal from the first data polarity inversion signal summer to invert the output data.

9. (Currently Amended): The liquid crystal display according to claim 3, wherein the second data polarity inversion signal output part includes[[,]] a multiplexor receiving a second polarity inversion signal from the second data polarity inversion signal summer to invert the output data.

10. (Canceled).

11. (Currently Amended): A method of driving a liquid crystal display having first and second data polarity inversion drivers, the method comprising:

dividing input data [[by]] into a first set of data and a second set of data;

inputting the first and second sets of data to the first and second data polarity inversion drivers, respectively;

determining a ~~the~~ number of first and second data transitions in the first and second sets of data, respectively; and

inverting a polarity of the first and second sets of data in accordance with the determined results,

wherein the first set of data is odd-numbered bits of the input data and the second set of data is even-numbered bits of the input data.

12. (Currently Amended): The method according to claim 11, wherein inverting [[a]] the polarity of the first and second sets of data includes: [[,]]

comparing current ~~first~~ data with previous ~~odd~~ data to determine whether ~~there is~~ the first and second data transitions have occurred;

adding the number of the first and second sets of data having the first and second data transitions; and

inverting the first and second sets of data if the number of the added data is more than a half of a total number of the input data bit and outputting the input data without an inversion if the number of the added data is less than or equal to a half of the total number of the input data bit.

13. (Canceled).

14. (Original): The method according to claim 12, wherein the total number of the input data bit is 18.

15. (Original): The method according to claim 12, wherein the number of first and second data bits is 9.

16. (New): A 2-port data polarity inverter for driving a liquid crystal display, comprising:

an odd data polarity inversion driver to generate a first inversion signal to invert odd-numbered input data bits when a first data transition is detected in the odd data; and

an even data polarity inversion driver to generate a second inversion signal to invert even-numbered input data bits when a second data transition is detected in the even data.

17. (New): The 2-port data polarity inverter according to claim 16,

wherein the odd data polarity inversion driver includes

a first data transition part to determine whether the first data transition has occurred in the odd data and outputting a first signal,

a first data polarity inversion signal summer to count the number of the first signal that a data polarity has changed according to the first data transition and determining whether an output level is high or low, and

a first data polarity inversion signal output part to receive the first signal and the determined output level from the first data transition part and the first data polarity inversion signal summer and outputting the first inversion signal; and

wherein the second data polarity inversion driver includes

a second data transition part to determine whether the second data transition has occurred in the even data and outputting a second signal,

a second data polarity inversion signal summer to count the number of the second signal that a data polarity has changed according to the second data

transition and determining whether an output level is high or low, and

a second data polarity inversion signal output part to receive the second signal and the determined output level from the second data transition part and the second data polarity inversion signal summer and outputting the second inversion signal.

18. (New): The 2-port data polarity inverter according to claim 17,

wherein the first data transition part includes first and second flip-flops and a first exclusive logical sum gate to compare current odd data with previous odd data to determine whether the first data transition has occurred in accordance with the compared result, and

wherein the second data transition part includes second and third flip-flops and a second exclusive logical sum gate to compare current even data with previous even data to determine whether the second data transition has occurred in accordance with the compared result.

19. (New): The 2-port data polarity inverter according to claim 17,

wherein the first data polarity inversion signal summer includes

a first adder to add the number of data with a data transition from the first data transition part, and

a first majority detector to determine whether the added number of the data is higher than a first reference value; and

wherein the second data polarity inversion signal summer includes

a second adder to add the number of data with a data transition from the

second data transition part; and

a second majority detector to determine whether the added number of the data is higher than a second reference value.

20. (New): The 2-port data polarity inverter according to claim 17,

wherein the first data polarity inversion signal output part includes a first multiplexor receive a first polarity inversion signal from the first data polarity inversion signal summer to invert the output data, and

wherein the second data polarity inversion signal output part includes a second multiplexor to receive a second polarity inversion signal from the second data polarity inversion signal summer to invert the output data.